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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/696,293	10/28/2003	Matthew B. Dubin	H17-25994A1 (256.055US2)	5120
128	7590	07/28/2005	EXAMINER	
HONEYWELL INTERNATIONAL INC. 101 COLUMBIA ROAD P O BOX 2245 MORRISTOWN, NJ 07962-2245			BLACKMAN, ROCHELLE ANN J	
			ART UNIT	PAPER NUMBER
			2851	

DATE MAILED: 07/28/2005

Please find below and/or attached an Office communication concerning this application or proceeding.

AK

<b>Office Action Summary</b>	<b>Application No.</b> 10/696,293	<b>Applicant(s)</b> DUBIN ET AL.	
	<b>Examiner</b> Rochelle Blackman	<b>Art Unit</b> 2851	

-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address --  
**Period for Reply**

A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) FROM THE MAILING DATE OF THIS COMMUNICATION.

- Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication.
- If the period for reply specified above is less than thirty (30) days, a reply within the statutory minimum of thirty (30) days will be considered timely.
- If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication.
- Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133). Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

**Status**

- 1) ☒ Responsive to communication(s) filed on 11 May 2005.
- 2a) ☐ This action is **FINAL**.                      2b) ☒ This action is non-final.
- 3) ☐ Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under *Ex parte Quayle*, 1935 C.D. 11, 453 O.G. 213.

**Disposition of Claims**

- 4) ☒ Claim(s) 1-28 is/are pending in the application.
- 4a) Of the above claim(s) \_\_\_\_\_ is/are withdrawn from consideration.
- 5) ☐ Claim(s) 27 and 28 is/are allowed.
- 6) ☐ Claim(s) 1-4, 8-14, 17-22, 25 and 26 is/are rejected.
- 7) ☐ Claim(s) 5-7, 15, 16, 23 and 24 is/are objected to.
- 8) ☐ Claim(s) \_\_\_\_\_ are subject to restriction and/or election requirement.

**Application Papers**

- 9) ☐ The specification is objected to by the Examiner.
- 10) ☒ The drawing(s) filed on 28 October 2003 is/are: a) ☒ accepted or b) ☐ objected to by the Examiner.  
Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).  
Replacement drawing sheet(s) including the correction is required if the drawing(s) is objected to. See 37 CFR 1.121(d).
- 11) ☐ The oath or declaration is objected to by the Examiner. Note the attached Office Action or form PTO-152.

**Priority under 35 U.S.C. § 119**

- 12) ☐ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
- a) ☐ All    b) ☐ Some \*    c) ☐ None of:
1. ☐ Certified copies of the priority documents have been received.
2. ☐ Certified copies of the priority documents have been received in Application No. \_\_\_\_\_.
3. ☐ Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)).
- \* See the attached detailed Office action for a list of the certified copies not received.

**Attachment(s)**

- |  |   |
|--|---|
| 1) <input checked="" type="checkbox"/> Notice of References Cited (PTO-892)  | 4) <input type="checkbox"/> Interview Summary (PTO-413)<br>Paper No(s)/Mail Date. _____ |
| 2) <input type="checkbox"/> Notice of Draftsperson's Patent Drawing Review (PTO-948)                                   | 5) <input type="checkbox"/> Notice of Informal Patent Application (PTO-152)             |
| 3) <input type="checkbox"/> Information Disclosure Statement(s) (PTO-1449 or PTO/SB/08)<br>Paper No(s)/Mail Date _____ | 6) <input type="checkbox"/> Other: _____  |

## **DETAILED ACTION**

### ***Continued Examination Under 37 CFR 1.114***

A request for continued examination under 37 CFR 1.114, including the fee set forth in 37 CFR 1.17(e), was filed in this application after final rejection. Since this application is eligible for continued examination under 37 CFR 1.114, and the fee set forth in 37 CFR 1.17(e) has been timely paid, the finality of the previous Office action has been withdrawn pursuant to 37 CFR 1.114. Applicant's submission filed on May 11, 2005 has been entered.

### ***Response to Arguments***

Applicant's arguments with respect to claims 1-28 have been considered but are moot in view of the new ground(s) of rejection.

### ***Claim Rejections - 35 USC § 102***

The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless –

(e) the invention was described in (1) an application for patent, published under section 122(b), by another filed in the United States before the invention by the applicant for patent or (2) a patent granted on an application for patent by another filed in the United States before the invention by the applicant for patent, except that an international application filed under the treaty defined in section 351(a) shall have the effects for purposes of this subsection of an application filed in the United States only if the international application designated the United States and was published under Article 21(2) of such treaty in the English language.

Claims 1-4, 8-14, 17-22, 25, and 26 are rejected under 35 U.S.C. 102(e) as being anticipated by Firester et al. (U.S. Patent No. 6,611,241).

Regarding claims 1 and 12, Firester discloses a tiled display apparatus with distortion control (for example, see 100 of FIG. 2), comprising: an image generator (see 105 of FIG. 2 and *image source* in col. 4, lines 13-23 and col. 5, lines 30-33) that generates display signals indicative of a desired image; a plurality of display devices (for example, see 50 of FIG. 4, also shown in FIG. 3 containing elements 111-123, but not referenced), wherein each display device is subdivided into a plurality of sections (see 111-113 and 121-123 of FIGS. 3 and 4), and the plurality of sections for each display device are capable of displaying a plurality of sectional images in response to display control signals applied to that display device (the "plurality of sections" 111-113 and 121-123 are considered to be "capable" of performing the recited function), each display device including a dead-band region (see sections between "plurality of sections" 111-113 and 121-123 of "display devices" 50 in FIG. 3 and also see non-shaded sections of "display device" 50 in FIG. 4) between each pair of adjacent sections; a screen (see 102 of FIG. 4); a plurality of lens assemblies (see 117-119 and 127-129 of FIGS. 3 and 4) optically coupled to the plurality of display devices for projecting the sectional images of the display devices to form a tiled image on the screen, at least one lens assembly being configured to provide magnification having a magnitude of greater than 1 such that the respective projected sectional image on the screen is larger than the corresponding sectional image on the display device ("lens assemblies" 117-119 and 127-129 are considered to be "configured" to perform the recited function), wherein the

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lens assemblies provide magnification to merge adjacent projected sectional images together to eliminate the dead-band regions from the tiled image ("lens assemblies" 117-119 and 127-129 are also considered to be "configured" to perform the recited function); a detector (for example, see 108 of FIGS. 2 and 4) that detects the tiled image and generates feedback signals; and a display controller (see 106 and IP1-IP4 of FIG. 2), coupled to the display devices and the detector, that generates the display control signals to display a commanded image on the screen, receives the feedback signals from the detector, and uses the feedback signals to reduce distortion on the screen (see col. 5, lines 10-25)/ a display controller (see 106 and IP1-IP4 of FIG. 2), coupled to the image generator, the display devices, and the detector, wherein the display controller generates the display control signals in response to the display signals to display the tiled image on the screen, and also receives the feedback signals from the detector and reduces distortion of the tiled image using the feedback signals (see col. 5, lines 1-25).

Regarding claims 2 and 13, Firester discloses wherein the display controller generates the display control signals to display a desired image/pattern on the screen, the detector generates feedback signals representative of an actual image/pattern that is displayed, and the display controller uses the feedback signals to characterize an error between the desired image/pattern and the actual image/pattern (see function of 106 and 108 of FIGS. 2 and 4 and see col. 5, lines 10-25).

Regarding claim 3, Firester discloses wherein the display controller introduces an inverse error to subsequent display control signals to cancel out the error that was

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characterized (see function of 106 and IP1-IP4 of FIG. 2 and also see col. 5, lines 62 to col. 6, line 8).

Regarding claims 4 and 14, Firester discloses wherein the desired image/pattern is a grid of dots/commanded pattern (see “dots” in col. 3, lines 26-29).

Regarding claims 8 and 17, Firester discloses wherein the detector is located at a fixed location (see location of 108 in FIG. 2).

Regarding claim 9, Firester discloses wherein the display controller generates the display control signals to display an element at each of a plurality of commanded locations, and the detector detects a plurality of locations at which the element is actually displayed which correspond to the commanded locations and generates feedback signals representative thereof see function of 106 and IP1-IP4 of FIG. 2 and also see col. 5, lines 62 to col. 6, line 8).

Regarding claim 10, Firester discloses wherein the detector comprises multiple detectors (for example, see 250, 254, 258, 262, 266 of FIG. 7 and col. 9, lines 18-23).

Regarding claim 11, Firester discloses wherein the lens assemblies are optically symmetric (see 117-119 and 127-129 of FIGS. 3 and 4).

Regarding claims 18-22 and 25, the “method of generating a tiled display with distortion control” is similarly met by the features and functions of the above-mentioned elements of the “tiled display apparatus with distortion control” of claims 1-4, 8-11, and 12-14.

Regarding claim 26, the “apparatus for generating a tiled display with distortion control” is similarly met by the above-mention elements of the “tiled display apparatus with distortion control” of claims 1 and/or 12.

***Allowable Subject Matter***

1. Claims 5-7, 15, 16, 23, and 24 are objected to as being dependent upon a rejected base claim, but would be allowable if rewritten in independent form including all of the limitations of the base claim and any intervening claims.
2. Claims 27 and 28 are allowed.

***Conclusion***

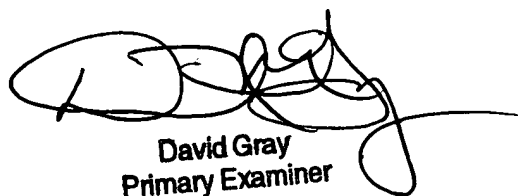
Any inquiry concerning this communication or earlier communications from the examiner should be directed to Rochelle Blackman whose telephone number is (571) 272-2113. The examiner can normally be reached on M-F 8:00-4:30.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Judy Nguyen can be reached on (571) 272-2258. The fax phone number for the organization where this application or proceeding is assigned is 703-872-9306.

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Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see <http://pair-direct.uspto.gov>. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

RB



David Gray  
Primary Examiner